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FORM PTO-1449								Check Number (Optional) 100-14310 (P04927-C1)	Application No. New 161692255 (Gmt. of 09/024,653)
INFORMATION DISCLOSURE CITATION IN AN APPLICATION								Applicant(s) Terry Lin s	
(Use several sheets if necessary)								Filing Date Herewith	Group Art Unit Unknown 2815

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CL	5	2	1	0	4	3	7	05/11/93	Sawada et al.	257	392	08/10/92
CL	6	1	9	8	1	4	0	03/06/01	Muramoto et al.	257	392	09/08/99
CL	5	5	8	1	1	0	7	12/03/96	Kawamura et al.	257	392	12/14/94
CL	5	0	1	6	0	7	7	05/14/91	Sato et al.	357	41	08/22/86
CL	6	4	7	6	4	3	0	11/05/02	Schmitz et al.	257	274	09/07/00
CL	4	4	7	2	8	7	1	09/25/84	Green et al.	29	571	11/19/80
CL	6	0	2	7	9	7	8	02/22/00	Gardner et al.	438	289	01/28/97
CL	4	3	2	9	1	8	6	05/11/82	Kotecha et al.	148	1.5	12/20/79
CL	6	0	2	0	2	2	7	02/01/00	Bulucea	438	194	03/07/97
CL	5	2	4	5	2	0	7	09/14/93	Mikoshiba et al.	257	392	08/17/90
CL	6	0	4	0	6	1	0	03/21/00	Noguchi et al.	257	392	04/08/98
CL	4	2	0	5	3	3	0	05/27/80	Klein	357	23	04/17/78
CL	4	5	7	8	6	9	4	03/25/86	Ariizumi et al.	357	41	09/30/82

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CL	Richard S. Muller and Theodore I. Kamins, "Device Electronics for Integrated Circuits", Second Edition, pps. 477-479.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.